Workshop On DIGITAL SYSTEM DESIGN using VERILOG

The Institution of Engineers (India),
ET Division, Dept. of ECE,
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Workshop on **DIGITAL SYSTEM DESIGN using VERILOG** is to be conducted for the students interested in Digital System Design.

TOPICS TO BE COVERED:
Fundamental of Digital Electronics
Fundamentals of Verilog
Applications of Verilog in Digital System Design

Lecture will be delivered by course experts.

ELIGIBILITY:
2nd Year onwards any branch

REQUIREMENTS :
Laptop
Notebook
IEI Membership Card

SCHEDULE:
19th October,2013(Sat):3pm-6pm
20th October,2013(Sun):9am-12pm and3pm-6pm

FEES:
IEI Members: Rs. 1000
Non Members: Rs. 1500

For FEE payment contact the Coordinators.
For other details contact the President & Secretary of IEI,BIT Mesra.

Register yourself:
http://tinyurl.com/DSDworkshop