## Birla Institute of Technology, Mesra Ranchi - 835215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

## Advertisement for Post of Junior Research Fellow/Project Associate I

Applications are invited for the post of Junior Research Fellow (JRF)/ Project Associate I in the Department of Electronics and Communication Engineering, Birla Institute of Technology Mesra for SERB POWER funded project titled 'Mitigation of Electromagnetic Interference and Electromagnetic Compatibility Issues in Wearable IoT devices".

**Job duration**: 1 year extendable up to 3 years based on annual progress

**Number of posts**: 1 (JRF or Project Associate I)

## **Eligibility:**

ME/M.Tech/BE/B.Tech with GATE/NET/Equivalent qualification in Electronics and Communication with specialization in Microwave Engineering (**For JRF**).

ME/M.Tech in Electronics and Communication with specialization in Microwave Engineering (**For PAI**). Qualifying degree with at least 75 % marks. The candidate should have a strong inclination towards research in EMI and EMC related domain with knowledge of CST, and HFSS simulation tools. Enrolment in Ph.D. program would be desired.

**Please Note**: Preference will be given for JRF position and upon unavailability of JRF, Project Associate I will be considered.

**Salary**: Rs. 31000/- +16% HRA per month for first two years, Rs. 35000/- +16% HRA pm for 3<sup>rd</sup> year (For JRF). Rs. 25000/-+ 16 % HRA per month for first two years & 28,000/- + 16 % HRA pm for 3<sup>rd</sup> year (For Project Associate I)

**How to apply:** Send your application with an updated CV through e-mail to ngupta@bitmesra.ac.in and cc to itusnigdh@bitmesra.ac.in and ar.src@bitmesra.ac.in by 15<sup>th</sup> **October 2022.** The subject of the email should be: Application for the post of JRF/Project Associate I. Candidates are advised to also send the scanned copy of certificates; grade cards/mark sheets, publications, photo-identity proof etc. as a **single pdf file** with the application. The selected candidates would be intimated by email on 16<sup>th</sup> October 2022.

**Venue, date & time of interview:** The on-line/offline interview will be held on 18<sup>th</sup> October 2022, in the Department of Electronics and Communication Engineering at 10:00 AM. No TA/DA will be provided for attending the interview.

Registrar