## BIRLA INSTITUTE OF TECHNOLOGY MESRA, RANCHI (END SEMESTER EXAMINATION)

CLASS: BRANCH	IMSC/MSC SE : PHYSICS SE	SEMESTER : VIII/II SESSION : SP/2023		
TIME:	SUBJECT: PH410 ELECTRONIC DEVICES & CIRCUITS 3 HOURS FI	FULL MARKS: 50		
<ul> <li>INSTRUCTIONS:</li> <li>1. The question paper contains 5 questions each of 10 marks and total 50 marks.</li> <li>2. Attempt all questions.</li> <li>3. The missing data, if any, may be assumed suitably.</li> <li>4. Before attempting the question paper, be sure that you have got the correct question paper.</li> <li>5. Tables/Data hand book/Graph paper etc. to be supplied to the candidates in the examination hall.</li> </ul>				
Q.1(a) Q.1(b)	Write a short note on Varactor diode and explain its tuning ratio and figure of merits Derive the I-V characteristics of a solar cell and the expression for maximum output power?	[5] [5]	1 1	вс 2 2,4
Q.2(a) Q.2(b)	Explain the process of Optical lithography and its limitations? Write a short note on process of crystal growth and wafer preparation?	[5] [5]	2 2	2,3 2
Q.3(a)	Mathematically establish relation between gain-bandwidth product for network with	[5]	3	4,5
Q.3(b)	For the network in the figure below determine: (a) $r_e$ (b) $Z_i$ (c) $Z_o$ ( $r_o = \infty \Omega$ ) (d) $A_v$ ( $r_o = \infty \Omega$ ) (e) The parameters of parts (b) through (d) if $r_o = 1/h_{oe} = 50 \text{ k}\Omega$ and	[5]	3	5,6

- compare results. 22 V  $I_{o}$   $6.8 K\Omega 10 \mu F$   $V_{i}$   $I_{i}$   $Z_{i}$   $8.2 K\Omega$   $I_{i}$   $S.2 K\Omega$   $I_{i}$   $I_{i}$ I
- Q.4(a) Derive an expression for the frequency response of the Op-amp and discuss the [5] 4 3,4 method of dominant-pole compensation?
- Q.4(b) Describe the frequency response of an ideal differentiator? How the limitation of an [5] 4,5 4,5 ideal differentiator can be overcome in a practical differentiator?
- Q.5(a) Explain the construction of a 555 timer circuit and its application as astable [5] 5 3 multivibrator?
- Q.5(b) Discuss the working of R-2R Ladder Digital to Analog convertor? How it is better than [5] 5 3 binary weighted type resistor D/A converter?

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