BIRLA INSTITUTE OF TECHNOLOGY, MESRA, RANCHI (MID SEMESTER EXAMINATION SP2023)

CLASS: BRANCH:		SEMESTER : VI SESSION : SP2023 ECT: EE353 POWER ELECTRONICS FULL MARKS: 25		
TIME:	SUBJECT: EE353 POWER ELECTRONICS 02 Hours FU			
INSTRUCTIONS: 1. The question paper contains 5 questions each of 5 marks and total 25 marks. 2. Attempt all questions. 3. The missing data, if any, may be assumed suitably. 4. Tables/Data handbook/Graph paper etc., if applicable, will be supplied to the candidates				
Q.1(a) Q.1(b)	Discuss the ideal and practical <i>i</i> -v characteristics of a power diode. Explain with the help of a neat diagram the reverse recovery characteristics of pow diodes.	[2] ver [3]	CO 1 1	BL 2 2
Q.2(a) Q.2(b)	State the necessary condition to turn-off a thyristor? Using two transistor model of a thyristor, derive the expression of anode current.	[2] [3]	1 1	1 2
Q.3(a) Q.3(b)	State the differences between BJT and Power MOSFET. Draw an explain the transfer and output characteristics of n-channel Power MOSFET.	[2] [3]	1 1	1 1
Q.4(a) Q.4(b)	State the turn-on conditions for SCR triggering. Define latching and holding currents as applicable to an SCR. Show these currents on static i-v characteristics.	[2] its [3]	1 1	1 1
Q.5(a) Q.5(b)	Explain the switching characteristics of Power MOSFET with proper waveforms. Explain Class F line commutation technique for a thyristor.	[2] [3]	1 1	2 1

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