

BIRLA INSTITUTE OF TECHNOLOGY, MESRA, RANCHI, INDIA

MID SEMESTER EXAMINATION SCHEDULE SP/2020

BE / IMSC - ADDITIONAL SEMESTER ONLY

MESRA / PATNA / DEOGHAR

DATE: 19.02.2020

FINAL

DATE/DAY	CSE / ECE / EEE	IMSC / MECH
26/02/2020 WEDNESDAY	CS4109 COMPUTER SYSTEM ARCH. [P]	IMM4001 MATHEMATICS-IV [M1]
	EE6201 CONTROL THEORY [PD]	ME4001 HEAT POWER CONVERSION [M1]
	EE6201 CONTROL THEORY [P]	XXXXXX
27/02/2020 THURSDAY	CH1401 ENGG. CHEMISTRY [P]	IMM2001 MATHEMATICS-II [M1]
	CH1401 ENGG. CHEMISTRY [D]	
	EC4205 MICROPROCESSOR & MICROCONTROLLER [P]	ME6003 HEAT & MASS TRANSFER [M1]
	EC6203 FIBER OPTICS COMM. SYSTEM [P]	
28/02/2020 FRIDAY	EE6205 INDUSTRIAL DRIVES & CONTROL [P]	XXXXXX
	EC3201 DIGITAL ELECTRONICS [P]	XXXXXX
	EC3205 SEMICONDUCTOR DEVICES [PD]	ME6005 DYNAMICS OF MACHINES [M1]
	EC6201 INTELLIGENT INSTRUMENTATION [P]	
29/02/2020 SATURDAY	EE6203 POWER SYSTEM - II [P]	XXXXXX
	MA2201 ADVANCE ENGG. MATHEMATICS [P]	XXXXXX
	EC4207 ELECTROMAGNETIC THEORY [P]	
	EC7201 MOBILE & CELLULAR COMMUNICATION [D]	XXXXXX
02/03/2020 MONDAY	MA2201 ADVANCE ENGG. MATHEMATICS [P]	XXXXXX
	MA2201 ADVANCE ENGG. MATHEMATICS [P]	XXXXXX
	MA4109 PROBABILITY STATISTICS & NUM. TECH. [P]	XXXXXX
	BT3021 BIOLOGICAL SCIENCE [D]	
03/03/2020 TUESDAY	CS2301 FUNDAMENTAL OF DATA STRUCTURE [P]	CS2301 FUNDAMENTAL OF DATA STRUCTURE [M1]
	EC2001 PRINCIPLES OF ELECTRONICS ENGG. [P]	
	EE4203 ELECTRICAL MACHINES - I [P]	XXXXXX
	PH1201 PHYSICS [P]	XXXXXX
04/03/2020 WEDNESDAY	EC6205 COMPUTER NETWORKING [DP]	ME6007 DESIGN OF MECHANICAL SYSTEM [M1]
	EE4209 ENGINEERING ELECTROMAGNETICS [P]	XXXXXX
	AM1201 ENGINEERING MECHANICS [P]	XXXXXX
	EC4203 ANALOG COMMUNICATION SYSTEM [P]	ME4003 MECHANICS OF MATERIALS [M1]
05/03/2020 THURSDAY	MEC2137 WIRELESS NETWORKS [D]	
	EE5203 ELECTRICAL MACHINES - II [P]	XXXXXX
	EE2201 PRINCIPLES OF ELECTRICAL ENGG. [P]	XXXXXX
	EE2201 PRINCIPLES OF ELECTRICAL ENGG. [P]	XXXXXX
06/03/2020 FRIDAY	MEC2163 SPEECH PROCESSING & RECOGNITION [P]	XXXXXX
	XXXXXX	XXXXXX
	XXXXXX	XXXXXX
	MEC1125 INFORMATION THEORY & CODING [P]	XXXXXX
07/03/2020 SATURDAY	XXXXXX	XXXXXX
	XXXXXX	XXXXXX
	EC4201 VLSI DESIGN [P]	XXXXXX
	XXXXXX	XXXXXX

TIME : 10:00 A.M. TO 11.30 A.M.

NOTES :

1. TEMPORARY ABSENCE DURING THE EXAMINATION HOURS IS NOT ALLOWED.
2. CANDIDATES ARE ADVISED TO READ THE "INSTRUCTION TO THE CANDIDATES" PRINTED ON THE COVER PAGE OF ANSWER BOOK.
3. CANDIDATES MUST BRING IDENTITY CARD WITHOUT WHICH THEY WILL NOT BE PERMITTED TO ENTER THE EXAMINATION HALL.
4. CANDIDATES ARE NOT ALLOWED TO ENTER THE EXAMINATION HALL WITH MOBILE PHONE, BOOKS & OTHER PAPERS. INVIGILATORS, EXAMINATION DEPARTMENT WILL NOT BE RESPONSIBLE, IN CASE OF ANY LOSS FOR THE SAME.



CONTROLLER OF EXAMINATION

COPY TO: 1. ALL DEANS, 2. ALL HOD, 3. DIRECTOR EXTN. CENTRE, 4. COORDINATOR, SEMESTER PROGRAMME, 5. REGISTRAR, 6. P.S. TO V.C., 7. EXAMINATION NOTICE BOARD.