



w.e.f.:08.01.2024

**Student  
TIME TABLE**

REVISED

<b>Department:</b>	<b>ECE</b>		<b>Programm:</b>	<b>M.Tech VLSI Design &amp; Embedded System</b>		<b>Branch: ECE</b>	<b>Semester: II</b>				
<b>Days</b>	<b>Period/Time/Subject/Room No. 254</b>										
	<b>I</b>	<b>II</b>	<b>III</b>	<b>IV</b>	<b>V</b>	<b>VI</b>	<b>VII</b>	<b>VIII</b>	<b>IX</b>		
	08:00-08:50	09:00-09.50	10:00-10.50	11:00-11.50	12:00-12.50	13:30-14.20	14:30-15.20	15:30-16.20	16:30-17.20		
<b>Monday</b>	EC581 AVLSID/KK	EC583 DVLSID /VN	EC585 ES/SSS				EC587 ICT/RM	EC592R1 NDM/DG			
<b>Tuesday</b>						Lunch Break	EC592R1 NDM/DG	EC585 ES/SSS	EC587 ICT/RM	EC581 AVLSID/KK	
<b>Wednesday</b>	EC583 DVLSID /VN	EC592R1 NDM/DG	MT133 Communication Skills-II				EC582 VLSID LAB / KK				
<b>Thursday</b>							EC586 ES LAB / SSS				
<b>Friday</b>							EC583 DVLSID /VN	EC587 ICT/RM	EC585 ES/SSS	EC581 AVLSID/KK	

Course Code	Course Title	Credit	Teacher
EC581	Analog VLSI Design	3	Dr. Kalyan Koley (KK)
EC583	Digital VLSI Design	3	Dr. V. Nath (VN)
EC585	Embedded Systems	3	Dr. S.S.solanki (SSS)
EC587	IC Technology (PE-I)	3	Dr. R.Mishra (RM)
EC592R1	Nanoelctronics Devices and Materials (PE-II)	3	Dr. D.Gola (DG)
EC582	VLSI Design Lab	2	Dr. Kalyan Koley (KK)
EC586	Embedded Systems Lab	2	Dr. S.S.solanki (SSS)
MT133	Communication Skills-II	1.5	

*Prem Prakash*  
(Prem Prakash)  
Co-ordinator  
Central Time Table, BIT Mesra

*Katiz*  
Department Time Table  
Co-ordinator

*Chijuro*  
9.1.2024  
HOD

*S.S. Solanki*  
(Dr. S.S.Solanki)  
Dean (UG Studies)