

### REGISTRATION AND FEES:

For Registration Click : [Register Now](#)

Registration Fee (including GST) :

For Students : INR 300

For Faculty : INR 600

### MODE OF PAYMENT:

Payment should be done ONLINE before filling registration form.

For ONLINE Payment: [Click Here](#)

Steps for Payment: [Click Here](#)

### BOARDING & LODGING:

Accommodation with breakfast, lunch and dinner will be provided on additional payment basis (as per institute rules).

### IMPORTANT DATES:

Last date for Registration : 04/03/2024

Receipt of Acknowledgement : 05/03/2024

### ORGANIZING COMMITTEE:

*Patron :*

*Prof. Indranil Manna*

*Vice Chancellor, B.I.T. Mesra*

*Convenor :*

*Dr. Sanjay Kumar*

*Head, Dept. of E.C.E, B.I.T. Mesra*

*Coordinators*

*Dr. P. P. Dash (Assistant Prof., B.I.T. Mesra)*

*Dr. Deepti Gola (Assistant Prof., B.I.T. Mesra)*

*Members*

*Dr. G. K. Mishra (Assistant Prof., B.I.T. Mesra)*

*Dr. Santashraya Prasad (Assistant Prof., B.I.T. Mesra)*

*Dr. Rupesh Kumar (Assistant Prof., B.I.T. Mesra)*

*Technical Support*

*Dr. Bibha Kumari*

*Mr. Dheeraj Kumar*

*Mr. Hiranmoy Kar*

### CONTACT PERSONS :

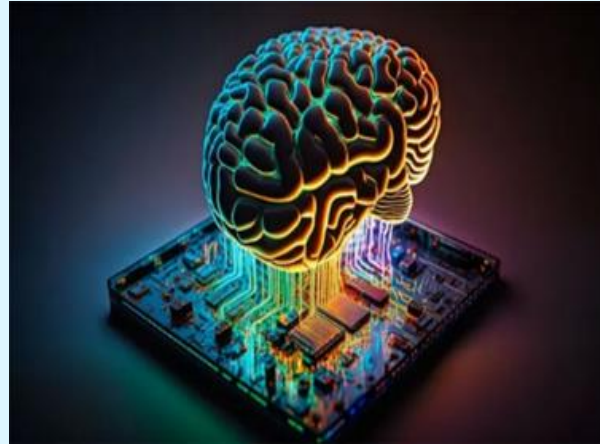
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Email : [ppdash@bitmesra.ac.in](mailto:ppdash@bitmesra.ac.in)

*Dr. D. Gola :* Mob. : +91-9411926012

Email : [deeptigola@bitmesra.ac.in](mailto:deeptigola@bitmesra.ac.in)

## NASD – 2024 (07<sup>th</sup> - 11<sup>th</sup> March 2024)



### General Information

- The Short-Term Course will be conducted in the **HYBRID MODE**.
- The detailed schedule of the course will be announced at later stage and intimated to registered candidates.
- The certificates will be provided after successful completion of the course.
- Participants joining ONLINE will be provided e-certificates.
- E-content of the course will be provided after successful completion of the course.

**Limited Seats Available!  
Reserve Your Spot Now!**

### Short Term Course on

## Neuroevolution-based Advanced Semiconductor Devices

(07<sup>th</sup> - 11<sup>th</sup> March 2024)

(Hybrid Mode)



**Dr. P. P. Dash  
Dr. Deepti Gola  
(Coordinators)**



**Dept. of Electronics & Comm. Engg.  
Birla Institute of Technology Mesra  
Ranchi - 835215**

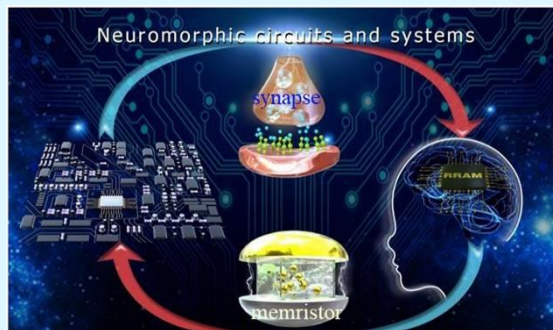
### **COURSE OBJECTIVE:**

The primary goal of the STC is to offer research scholars, faculty members, and industry personnel a unique opportunity to delve into the intersection of Artificial Intelligence and Machine Learning (AIML) within the Semiconductor devices domain. The course is structured to bridge the gap between theoretical understanding and practical implementation in the dynamic realm of AIML within Semiconductor devices.

Academicians/experts in the concerned field from IITs/NITs/Industry (R&D) and other reputed institutes will be invited to deliver lectures in the program. Hands-on training sessions will be conducted by experts from industry/academia. Participants can expect a well-rounded learning experience, enabling them to apply the acquired knowledge effectively in both academic and industrial settings.

### **COURSE HIGHLIGHTS:**

- ❖ Overview of Advanced Semiconductor Devices.
- ❖ Insights of Neuromorphic Computing
- ❖ Evolution of Semiconductor Devices in the context of Neuromorphic Computing.
- ❖ Key principles and concepts in Artificial Intelligence and Machine Learning.
- ❖ Understanding the synergy between Neuromorphic Computing and Advanced Semiconductor Devices.
- ❖ Practical sessions for semiconductor devices and AIML techniques will be conducted.



### **TARGET ATTENDEES :**

ME / M Tech / M.Sc. / PhD Scholars/ Faculty Members/Scientist/Engineers in relevant discipline

### **ABOUT DEPARTMENT OF ECE:**

The main objective of the department is to impart high quality education and research. The major research areas of the department include Communication engineering, Instrumentation, Wireless Communication, Microwave Engineering, Signal Processing, and VLSI Design. The EC department is handling several research projects sponsored by external funding agencies.

### **ABOUT BIT MESRA:**

Birla Institute of Technology (BIT) Mesra, located in Ranchi, Jharkhand, India, stands as a prominent institution in the field of higher education and technical research. Established in 1955 by the visionary industrialist B.M. Birla, the institute has consistently maintained its reputation for academic excellence and innovation. BIT Mesra offers a diverse range of undergraduate, postgraduate, and doctoral programs across various disciplines, including engineering, management, pharmacy, and applied sciences.

The sprawling campus, set against the picturesque backdrop of the Chhotanagpur plateau, provides a conducive environment for learning and research. BIT Mesra has forged strong ties with leading industries and research organizations, facilitating internships, placements, and collaborative research projects. With a rich history and a commitment to nurturing talent, BIT Mesra, continues to play a pivotal role in shaping the future of aspiring professionals and contributing to the advancement of knowledge and technology.



### **ABOUT RANCHI:**

Ranchi, in its modern form, is the capital city of the Indian state of Jharkhand. The city has a moderate climate and was the summer capital of Bihar until Jharkhand was separated in the year 2000. It is popularly known as a “City of Water Falls”. Nesting amidst greenery on all sides, BIT campus is approximately 14 km from Ranchi railway station. The nearest airports are Ranchi, Patna, Kolkata, Rourkela which are well connected by trains.