## Department of Computer Science & Engineering Syllabus for PhD Entrance Test

The syllabus for the Basic test will be as follows

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Aita	
Basic Mathematics:	Discrete structures (sets, graphs and tress, algebraic structures, matrix algebra, elementary counting and probability), elementary calculus, linear algebra, 2-3D geometry.
Programming Aptitude	Ability to write and analyze programs in C/C++ to solve simple problems. Use of elementary data structures such as arrays, lists, stacks, queues, trees, graphs. Familiarity with recursion, pointers and file handling. Ability to differentiate procedure & OOP concepts, writing loop invariants and assertions.
Computer Science and	
Engineering [Core 7 Areas]	
Algorithm Design and Analysis:	Analysis of Algorithm, Divide and Conquer Dynamic Programming, Greedy, Backtracking, P, NP, NP-C, NP-Hard Class Problems.
Artificial Intelligence	Knowledge Representation and organization, Search and control Strategies, Matching Techniques, Expert System Architecture.
Theory of Computation&	Finite State Automata (FSM)-deterministic and non-deterministic, Regular
Compiler Construction	Expression, Grammar, Derivation, Ambiguous grammar, Idea on Left-factoring and left-recursion, Push-down automata, Turing Machine, Halting problem of Turing Machine and undecidable Language, Translators, Phases of Compiler: Lexical analysis, Syntax
Commutan Cuanking and	analysis, Intermediate code generation, Code optimization, Run-Time Graphics Display, Scan Conversion, 2D and 3D Transformations,
Computer Graphics and Image Processing:	Projections, 2D and 3D viewing and Clipping, Splines, Visible surface Detection, Illumination Models and Surface Rendering, Image Enhancement, Segmentation, Restoration, Segmentation, Image Compression.
Computer Networks	Fundamental communication theory, Data Link Layer protocols, Internetworking, Transport Protocols, Application Layer Protocols.
Database Management Systems and Software Engineering	
Operating Systems	Basic concepts, Process Scheduling, Synchronization, Deadlock, Memory and Virtual Memory Management
Computer System Organizationand Architecture	Machine instructions and addressing modes, ALU, Data-path and control unit, Instruction pipelining, Memory hierarchy: cache, main memory and secondary storage; I/O interface.