



Call For Papers International Conference on Communication and Smart Devices 2026 - ICCoSD (Hybrid)

'Silicon to Intelligence'

17-18 December 2026

About BIT Mesra

BIT Mesra is a "Deemed to be University" under Sec. 3 of the U.G.C. Act 1956. Established in 1955 by visionary industrialist Mr. B.M. Birla, the institute has been at the forefront of technical education in India with a rich heritage of academic excellence.

Theme

Silicon to Intelligence: This year's theme 'From Silicon to Intelligence' explores the journey from semiconductor fundamentals to smart device applications, bridging core technologies with real-world innovations in communication systems.

About The Conference

The International Conference on Communication and Smart Devices (ICCoSD-26) is organized by the Department of Electronics and Communication Engineering at Birla Institute of Technology Mesra. Since its inception, the department has undergone remarkable growth in various aspects, including student and faculty numbers, infrastructure development, and learning resources.

This premier conference brings together researchers, academics, and industry professionals to discuss the latest research and development related to communication systems and smart device technologies.

Speakers



Dr. Harayuki Hiratani
CEO, Takasago Fluidic Systems, Japan



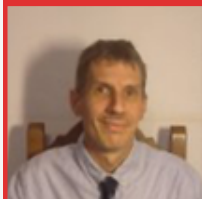
Prof. Juan Rafael Orozco-Arroyave
School of Engineering, Universidad de Antioquia, Medellin, Colombia



Prof. Tapan K. Gandhi
Department of Electrical Engineering, Indian Institute of Technology, Delhi



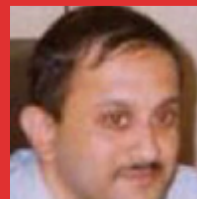
Dr. Satyasai Jagannath Nanda
Department of Electronics and Communication Engineering, Malaviya National Institute of Technology Jaipur



Prof. Alexander Kocian
Dept. Computer Science, University of Pisa, Pisa, Italy



Prof. Bidyadhar Subudhi
School of Electrical Sciences, Indian Institute of Technology, Goa



Prof. Vikram M. Gadre
Department of Electrical Engineering, Indian Institute of Technology Bombay



Dr. Aatmesh Shrivastava
Electrical and Computer Engineering, Northeastern University, Boston Massachusetts, United States

Tracks

- VLSI & Embedded Systems:** VLSI Design, Nano Electronics, IC Technology, Embedded Systems
- Signal & Image Processing:** Acoustic Signal Processing, Biomedical Signal Processing, Image Processing & Computer Vision, AI & ML Applications, IoT, Speech Processing
- Next Generation Communication Systems:** 5G and Beyond, Green Communication, Optical Communication, Intelligent Reflective Systems, Massive MIMO, RF, Microwave, and mmWave circuits
- Sensors and Devices:** MEMS Design, Fabrication and Packaging, Physical, Chemical and Biomedical Sensors, Micro/Nano Devices, NEMS and Microfluidic Systems, Wearable and Flexible Sensors, Energy Harvesting and Self-Powered MEMS Devices

Important Dates

- Paper Submission Opens **March 12, 2026**
- Paper Submission Closes **June 30, 2026 July 15, 2026**
- Acceptance Notification **September 30, 2026**
- Camera-Ready Manuscript **October 5, 2026**
- Early Bird Registration **October 10-20, 2026**
- Registration Deadline **November 1, 2026**
- Conference Date **December 17-18, 2026**

Registration Fee

REGISTRATION

Student (IEEE)	INR 7000 / \$180
Student (non-IEEE)	INR 8000/ \$200
Academician (IEEE)	INR 9000/ \$240
Academician (non-IEEE)	INR 10000/ \$250
Industry Professional (IEEE)	INR 11000 / \$290
Industry Professional (non-IEEE)	INR 12000/ \$300
Attendee	INR 4000 / \$100
Additional Paper	INR 5000 / \$100

ORGANISING COMMITTEE

TPC Chairs

Dr. Gajendra Kant Mishra
Dr. Soumya Sidhishwari
Dr. Sandeep Mukherjee
Dr. Md. Afaque Azam
Dr. Shubham Anand
Dr. Vijay Nath

Publication Chair

Dr. Sanjeet Kumar

Treasurer

Dr. Santashraya Prasad
Dr. Megha Dadel

Patron

Prof. Indranil Manna
Vice Chancellor, BIT Mesra

Chairman

Prof. Sanjay Kumar
HOD, ECE, BIT Mesra

Convenor

Dr. Sitanshu Sekhar Sahu

Co-Convenor

Dr. Richa Mishra
Dr. Rupesh Kumar Sinha

Secretary

Dr. Vishal H Shah
Dr. Dileep Kr Upadhyay

Scan This QR Code
or Visit The Link
Below For
Submitting Your
Paper

