REGISTRATION AND FEES:

For Registration Click : Register Now

Registration Fee (including GST) :
For Indian Nationals : ₹ 500/-

MODE OF PAYMENT:

Please pay the registration fee ONLINE before filling out the Registration form.

For ONLINE Payment:
Scan or use the details below for the payment.

SCAN & PAY

UPI ID : eictacademy@indianbk

Account Number: 50380476798
Account Name: E AND ICT ACADEMY
IFSC CODE: IDIB000B810
INDIAN BANK NIT Patna, Bihar

Fee payment is non-refundable.

BOARDING & LODGING:

***The registration fee includes lunch for the participant.**

Accommodation with breakfast and dinner will be provided on an *additional payment basis* (as per institute rules).

IMPORTANT DATES:

Last date for Registration : 03/07/2026

Receipt of Acknowledgement: 04/07/2026

ORGANIZING COMMITTEE:

Patron :

Prof. Indranil Manna
Vice Chancellor, B.I.T. Mesra

Chairman :

Dr. Sanjay Kumar
Head, Dept. of E.C.E, B.I.T. Mesra

Coordinator

Dr. Priyank Saxena (Assistant Prof., B.I.T. Mesra)

Co-coordinator

Dr. Kartik Mahto (Assistant Prof., B.I.T. Mesra)

Organizing Team

Prof. S. S. Solanki, Dr V. H. Shah, Dr S. S. Tripathy
Dr G. K. Mishra, Dr S. Sengupta, Dr Rupesh Kumar
Dr S. A. Prasad, Dr Richa Mishra, Dr V. Nath

CONTACT PERSONS :

Dr. P. Saxena: Mob. : +91-7250557586 / 9431597282
Email : priyanksaxena@bitmesra.ac.in

EIoT-I4.0
(6th – 11th July 2026)

Number of Seats available-60
Reserve Your Spot Now!

TARGET ATTENDEES :

Faculties of Degree/Diploma levels, Practising Engineers & Planners in Government & Industry, and Research Scholars can attend this FDP in Hybrid mode.

General Information

- The workshop will be conducted in the **Hybrid Mode.**
- The detailed schedule of the workshop will be announced at a later stage, updated on our website, and shared with the registered candidates.
- E-certificates will be issued to participants maintaining at least 75% attendance and completing the assessment process.
- **For More Information:**
- **: Click Here**

One Week Faculty Development Program on "Edge-Enabled IoT Systems for Industry 4.0 Applications" (6th – 11th July 2026)

Organized By

Dept. of Electronics & Comm. Engg.
Birla Institute of Technology Mesra
Ranchi - 835215

in association with

Electronics & ICT Academy,

NIT Patna

About the FDP:

The proposed Faculty Development Program aims to equip faculty participants with conceptual and practical knowledge of Edge-enabled IoT systems, embedded intelligence, and FPGA-based acceleration for Industry 4.0 applications. Participants will gain expertise in designing real-time IoT systems, deploying edge analytics, and implementing hardware-software co-design using FPGA SoCs. It includes

- 42 Hours Instructor-led offline/online Hands-on based learning & Interactive Query Sessions.
- Participants will get recorded sessions after completion of each day's sessions.
- E-Certificates will be given to participants who have more than 75% attendance in the FDP.

Course Content:

The Faculty Development Programme on Edge-Enabled IoT Systems for Industry 4.0 Applications provides a comprehensive understanding of cyber-physical systems, IoT architecture, and embedded platforms used in smart industrial environments. The programme covers real-time embedded systems and RTOS concepts, followed by edge computing and analytics for low-latency industrial applications. Advanced topics include Edge AI and Tiny-ML for predictive maintenance and vision-based monitoring, as well as FPGA-based hardware acceleration and SoC platforms for real-time signal processing and AI inference. The course integrates theory with practical insights and industry case studies to strengthen participants' capability in designing edge-to-cloud Industry 4.0 solutions.

Resource Persons:

The various talks and hands-on sessions during the FDP will be delivered by experts from industry, faculty members of IISc/IIT/NIT/IIEST, and other leading institutions.

Program Objective:

The objective of this Faculty Development Programme is to equip faculty members with comprehensive knowledge and practical skills to design, implement, and deploy Edge-Enabled IoT systems for Industry 4.0 applications. The programme aims to develop expertise in ARM Cortex-based embedded systems (microcontroller and processor-based platforms), Linux-based edge computing platforms such as Raspberry Pi, and FPGA-based hardware acceleration techniques for real-time data processing and Edge AI workloads. It also seeks to train participants to deploy machine learning models at the edge and implement industrial IoT communication protocols with security considerations. Through structured theory sessions and intensive hands-on laboratories, participants will gain the capability to design and demonstrate complete edge-to-cloud Industry 4.0 prototype systems suitable for teaching, research, and industrial collaboration.

Program Outcomes:

By the end of the program, the participants will be able to:

- Understand architecture and design of Edge IoT systems
- Design embedded systems for real-time sensing & control
- Develop edge computing applications using Raspberry Pi
- Integrate IoT devices with cloud platforms
- Implement FPGA-based hardware accelerators

ABOUT E&ICT Academy:

Ministry of Electronics and Information Technology, GOI has instituted seven Electronics and Information & Communications Technology (ICT) Academies, of which the academy of NIT Patna is one.

The Academy at NIT Patna aims to design and organise basic and specialised training programs in niche areas of electronics and ICT to develop the required knowledge base, skills, and tools, equipping the teaching community with better knowledge and understanding.

ABOUT DEPARTMENT OF ECE:

The ECE Department is supported by FIST and is NBA-accredited. The department's main objective is to provide high-quality education and conduct research. The department's major research areas include Communication Engineering, Instrumentation, Wireless Communication, Microwave Engineering, Signal Processing, and VLSI Design. The ECE department is managing several research projects funded by external agencies.

ABOUT BIT MESRA:

Birla Institute of Technology (BIT) Mesra, located in Ranchi, Jharkhand, India, is a prominent institution in higher education and technical research. Established in 1955 by the visionary industrialist B.M. Birla, the institute has consistently maintained its reputation for academic excellence and innovation. BIT Mesra offers a diverse range of undergraduate, postgraduate, and doctoral programs across various disciplines, including engineering, management, pharmacy, and applied sciences.

The sprawling campus, set against the picturesque backdrop of the Chhotanagpur plateau, provides a conducive environment for learning and research. BIT Mesra has forged strong ties with leading industries and research organizations, facilitating internships, placements, and collaborative research projects. With a rich history and a commitment to nurturing talent, BIT Mesra, continues to play a pivotal role in shaping the future of aspiring professionals and contributing to the advancement of knowledge and technology.